IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application of

Miwa WAKE et al.

Serial No. 10/766,587 : Group Art Unit: 2822

Filed: January 28, 2004 : Examiner: Thanh Y. Tran

For: METHOD OF MANUFACTURING :

A SEMICONDUCTOR

INTEGRATED CIRCUIT AND

SEMICONDUCTOR INTEGRATED

CIRCUIT : Docket No. S004-4839(DIV)

Mail Stop Amendment COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE

S I R:

In response to the Office Action dated March 10, 2006, applicants amend their application as follows:

STATUTORY DISCLAIMER FEE:

A check in the sum of \$130.00 is enclosed to cover the required statutory disclaimer fee. Should the check prove insufficient, or should other fees be required, authorization is hereby given to charge any such insufficiency or fees to our Deposit Account No. 01-0268.

MAILING CERTIFICATE ON PAGE 18